

ABSTRACT OF THE DISCLOSURE

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A semiconductor device that facilitates the layout designing of cells and power supply lines. The semiconductor device includes a first power supply line that corresponds to a first power supply voltage, a second power supply line that corresponds to a second power supply voltage, and a function block. A first standard cell is arranged in the function block and has a first power supply terminal connected to the first power supply line. A second standard cell has a second power supply terminal connected to the second power supply line. A level converter cell is aligned with the first and second standard cells and has a third power supply terminal connected to the first power supply line and a fourth power supply terminal connected to the second power supply line.